

FIG. 2

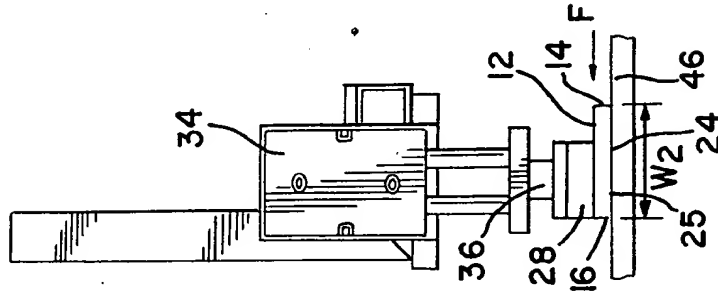


FIG. 1

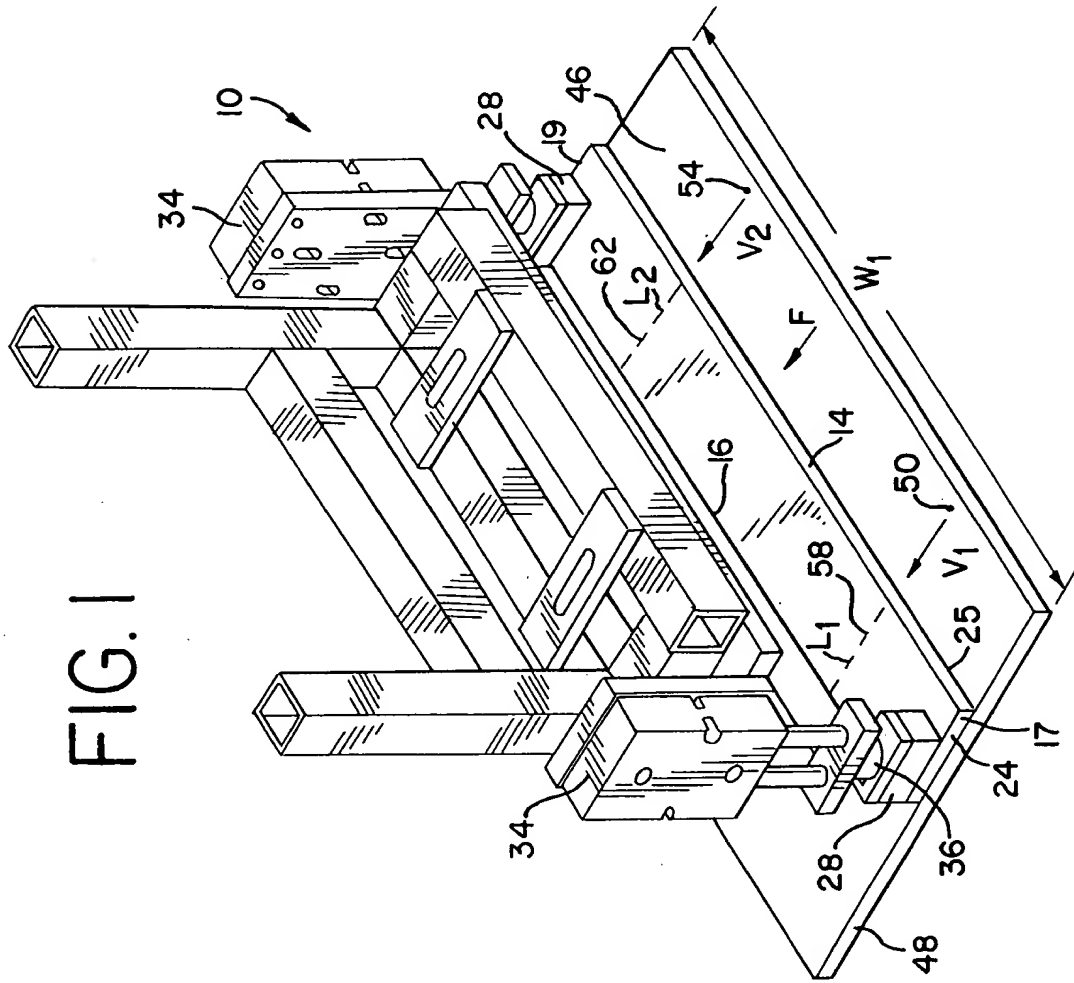


FIG. 3A

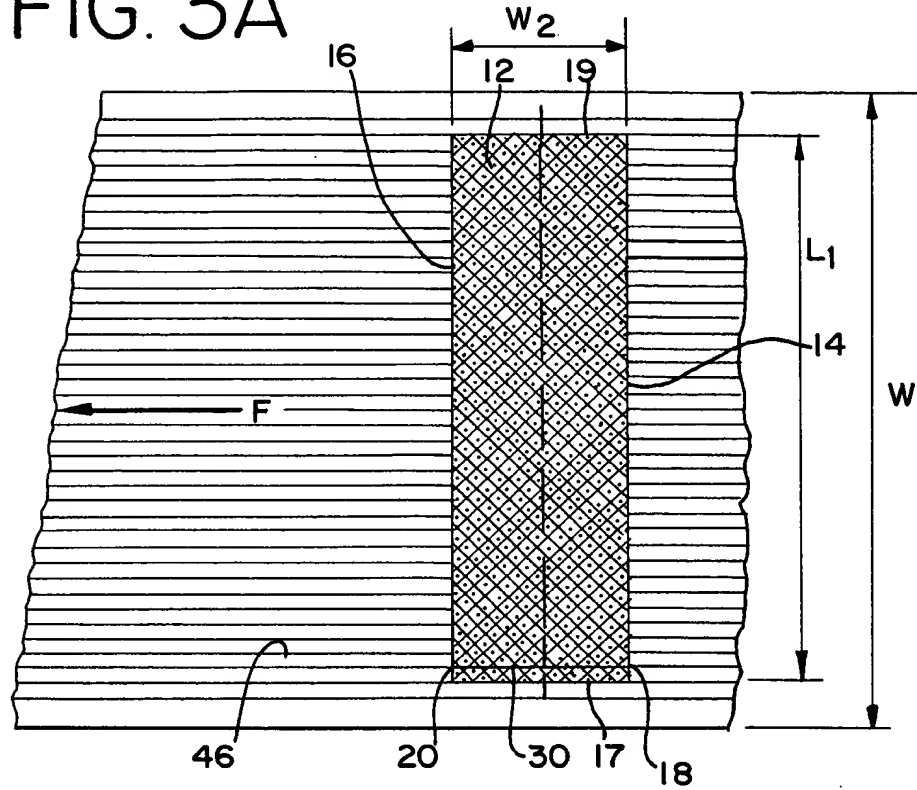


FIG. 3B

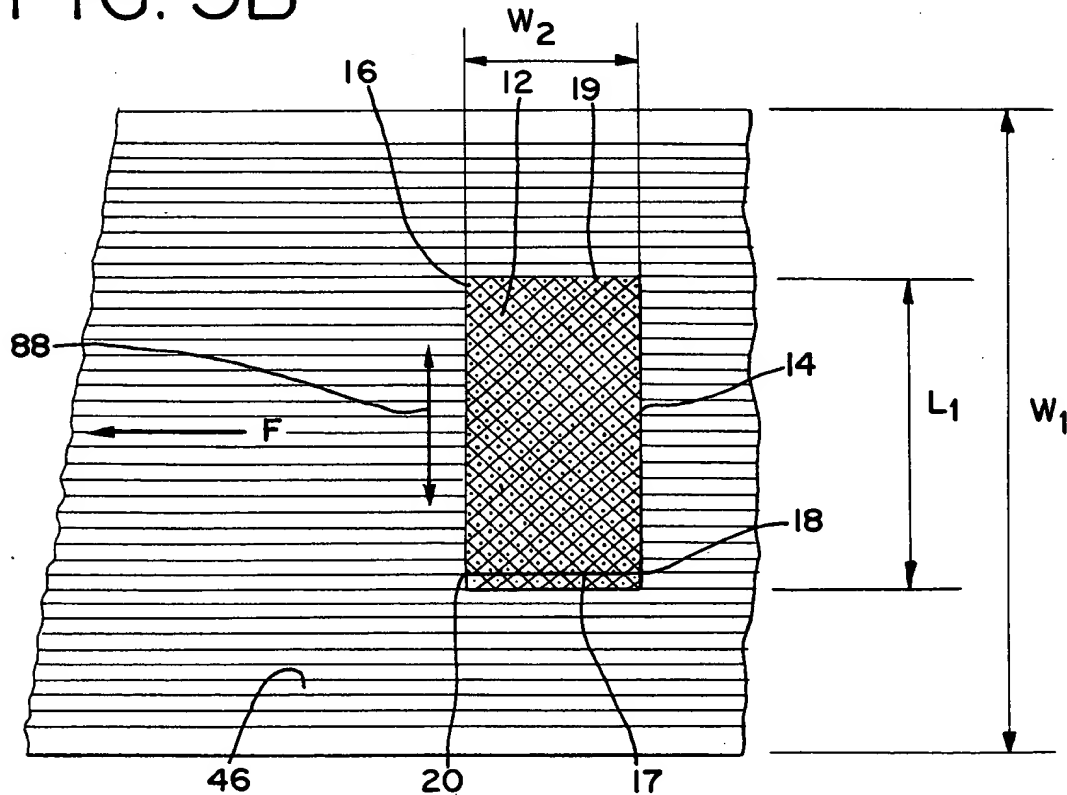


FIG. 4

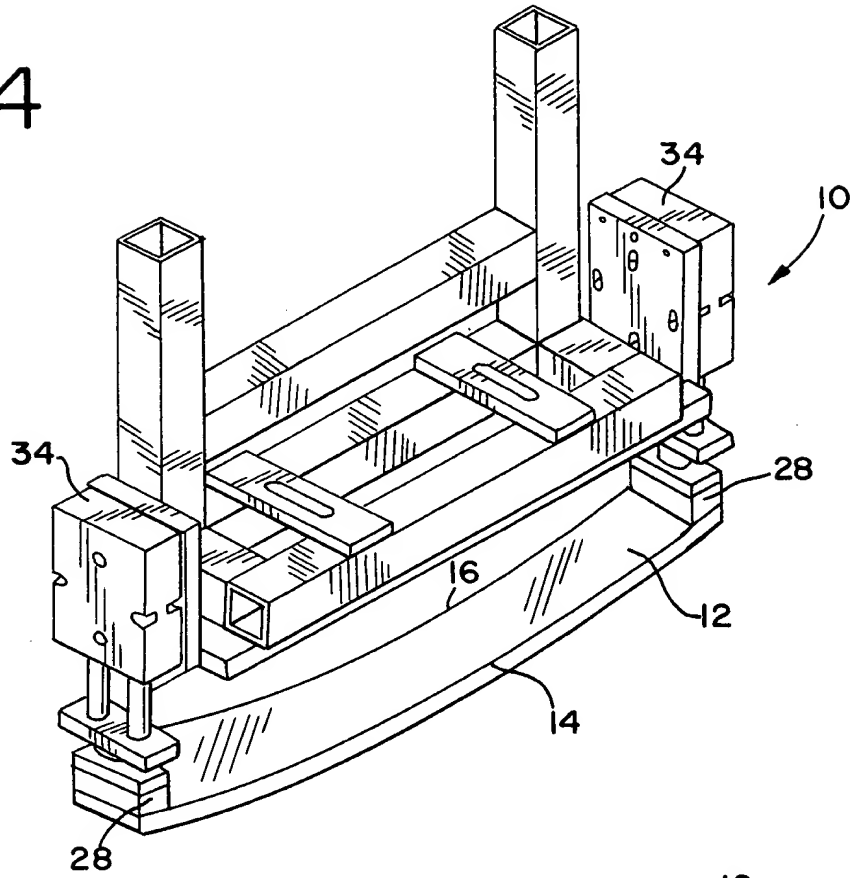


FIG. 5

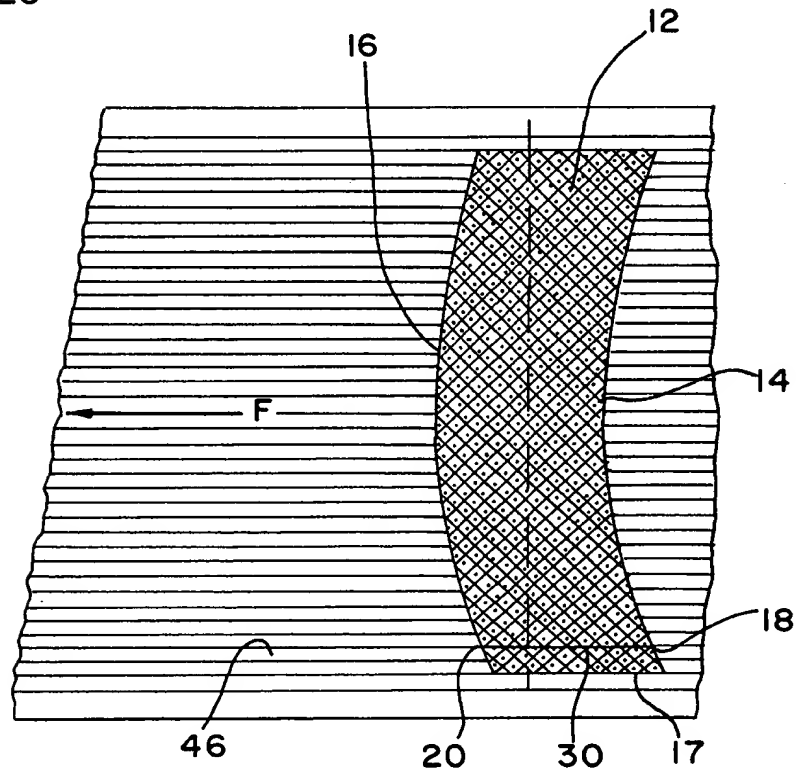


FIG. 4

FIG. 6

A schematic diagram of a circular structure, possibly a lens or a mirror, with various internal components and vectors. The diagram includes the following labeled elements:

- 46**: The outer circular boundary.
- 47**: A curved arrow indicating a direction of rotation or movement along the boundary.
- 50**: A vector V_1 pointing left from the top edge.
- 54**: A vector V_2 pointing left from the upper internal region.
- 58**: A point on the upper left edge of the circle.
- 31**: A dashed line segment extending from the top left towards the center.
- 62**: An angle or arc between two lines near the top center.
- L₁** and **L₂**: Labels for specific lines or paths.
- 20**, **18**, **21**, **12**, **14**, **16**: Various internal lines and segments.
- 10**: A dashed line segment extending from the center towards the bottom left.
- 90**: A point or center of a small circle at the bottom center.
- 92**: A point or center of a small circle at the bottom right.
- C** and **G**: Labels for specific points or regions at the bottom.

FIG. 8 is a block diagram of a data processing system. The system includes a central unit 32 connected to a host computer 38 via a bidirectional link 39. The central unit 32 is also connected to two input/output units 34. Each unit 34 is connected to a storage device 40 and a control unit 41. The control units 41 are connected to a common bus 12 via interface units 36. The bus 12 is connected to a power supply 17 and a control unit 19.

FIG. 9

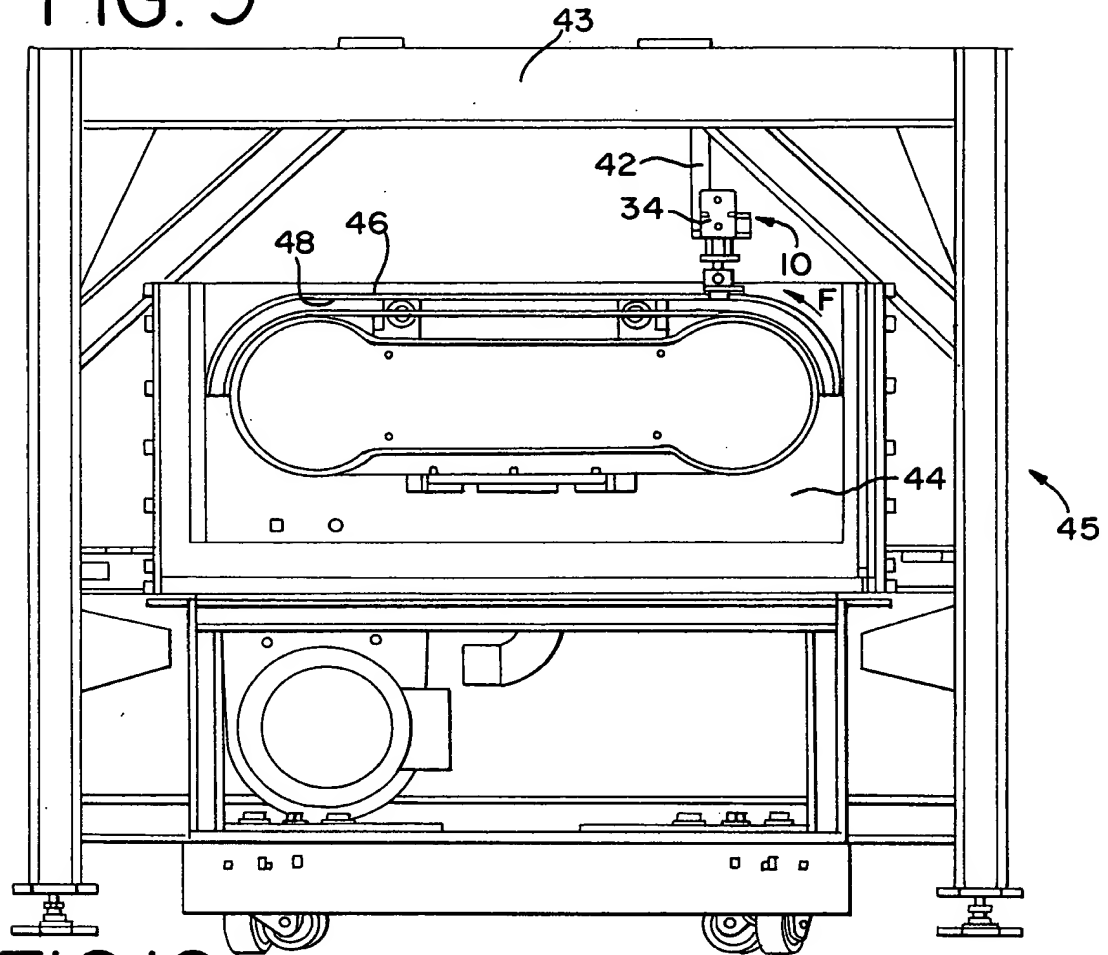


FIG. 10

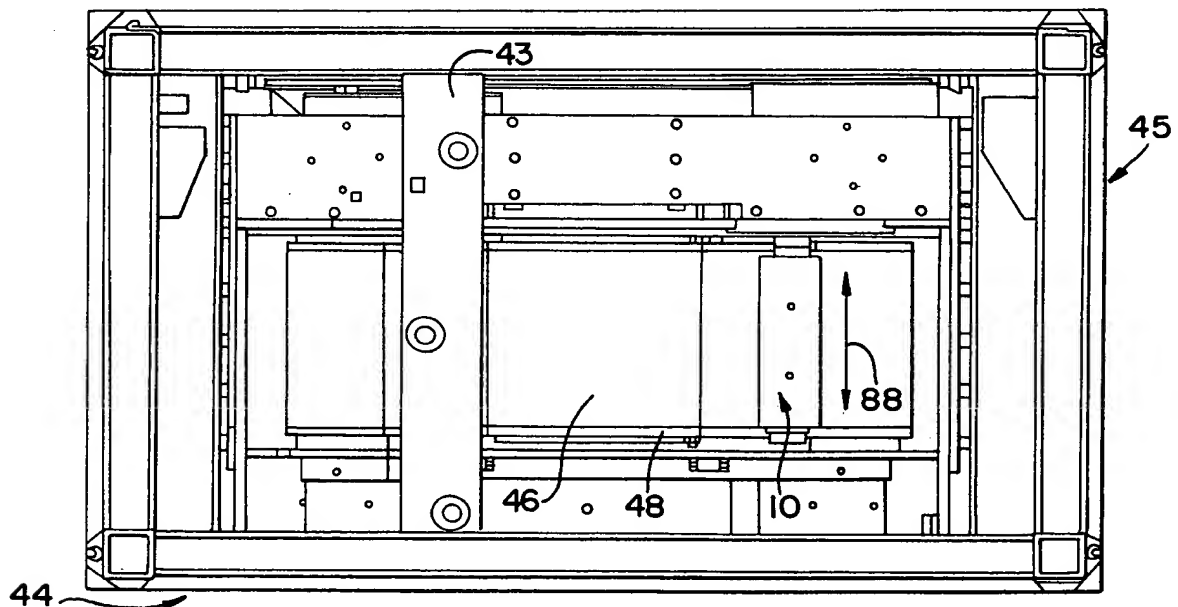
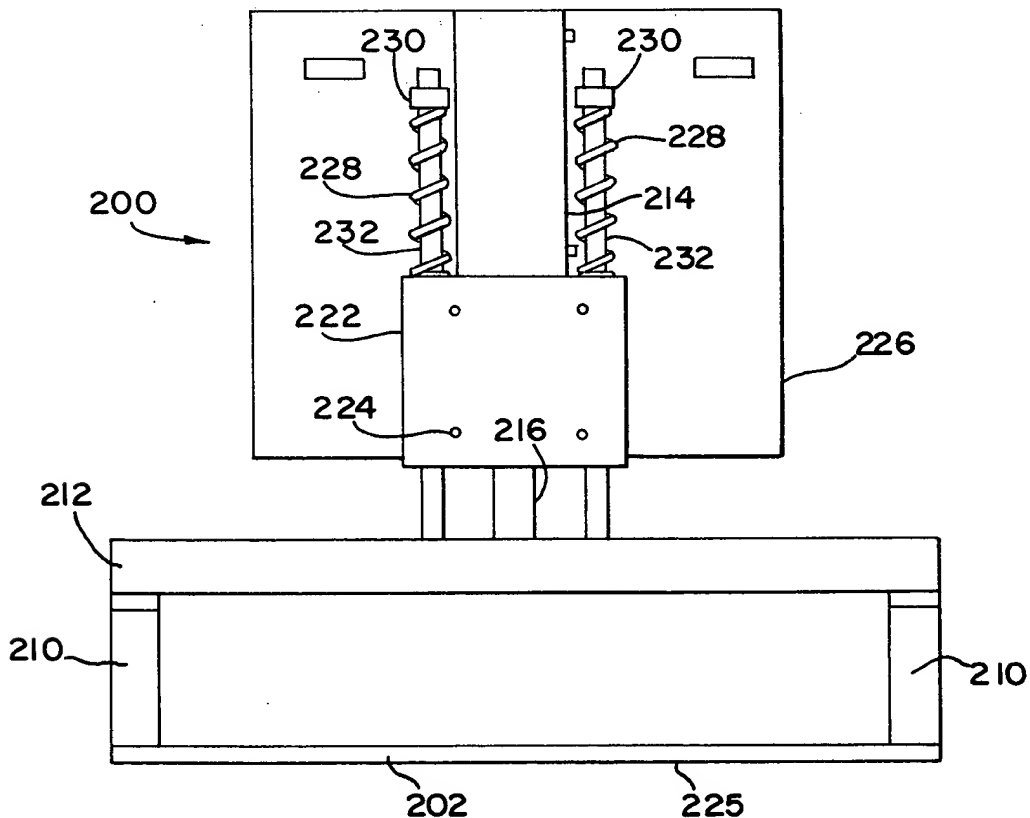


FIG. 12



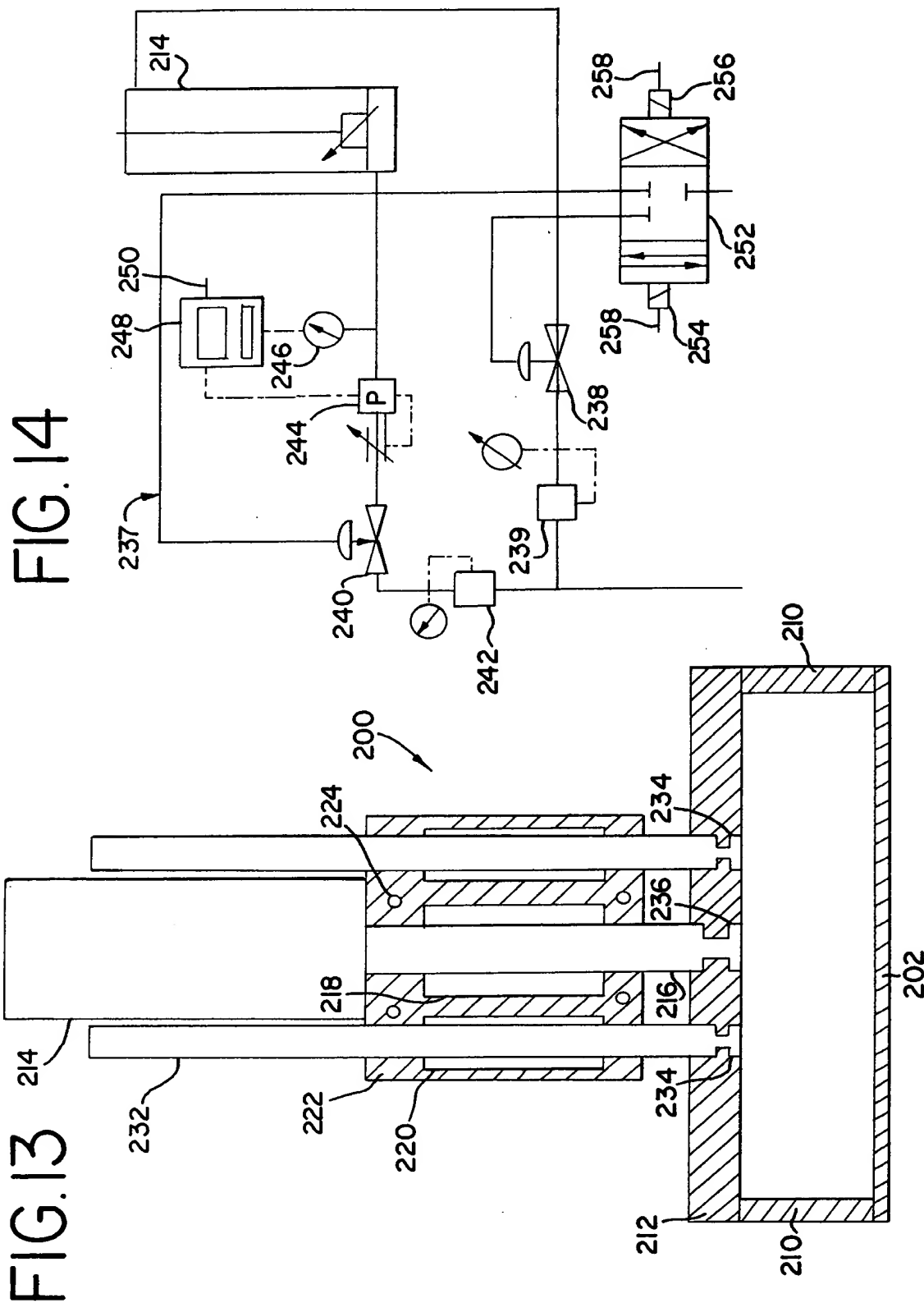


FIG. 13